



## PSC-ETH-2 Firmware Revision History

### *Legend:*

- B = Bug fix
- \$ = valuable improvement
- N = New feature

### *Changes from P0102 to P0110*

*24th of June, 2020*

- B Unit reset  
Spontaneous resets accrued when build in an SM-serie. Especially when a sequencer is started.
- \$ Updating  
When the PSC-ETH has no direct control over the output (e.g. with PSC-ETH EXT), updating will stall since the output must be discharge before the update mechanism will continue.  
Now the signal RSD is used to disable the output.

### *Changes from P0101 to P0102*

*05th of December, 2019*

- B Manual  
The equations for calibration are corrected.
- B Calibration  
Calibration commands of Vmon, lmon and lprg are more accurate now.
- \$ Front control  
Improved stability of the controls of the front functions via Ethernet or Web .

### *Changes from P0100 to P0101*

*21th of November, 2019*

- B Front lock  
Setting the front lock mode via Ethernet commands is functioning properly now.
- B Execution commands  
Commands send via Ethernet, related to output on/off and remote CV/CC do not cause malfunction anymore.
- \$ IP settings  
IP settings done via the DE\_Configurator are stored in memory automatically now.
- B Sequence Ethernet commands  
The commands *PROG:SEL:STEP ?*, *PROG:SEL:LABEL?* and *PROG:CAT?* are working properly now.
- B Output button  
Operation of the output mode button on the web page is corrected.